

Application Serial No.: 10/658,234
Attorney Docket No.: 0140115

In the Abstract:

- Please amend the Abstract, as follows:

A bias control circuit for controlling a bias circuit coupled to an amplifier transistor ~~is disclosed. An exemplary~~ The bias control circuit ~~comprises means for~~ receiving receives a control voltage, and ~~means for~~ actively adjusting adjusts an equivalent resistance of the bias control circuit responsive to the control voltage, wherein the equivalent resistance is established between ~~the~~ a first node and a reference voltage. In one embodiment, when the control voltage is increased, the equivalent resistance is gradually decreased and a current drawn by the bias control circuit is gradually increased, resulting in a quiescent current of the amplifier transistor being gradually increased.